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PAGE 3/8

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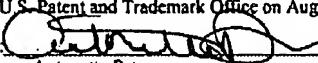
S/N 10/824942

PATENT**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant:	Mitsuhiko OTANI	Examiner:	KARIMY
Serial No.:	10/824942	Group Art Unit:	2815
Filed:	April 15, 2004	Docket No.:	10873.1453US01
Title:	SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE		

CERTIFICATE UNDER 37 CFR 1.6(d):

I hereby certify that this paper is being transmitted by facsimile to the U.S. Patent and Trademark Office on August 30, 2007.

By: 
Name: Antonette Peters**REQUEST FOR CONTINUED EXAMINATION UNDER 37 C.F.R. §1.114**

Mail Stop RCE
 Commissioner for Patents
 P.O. Box 1450
 Alexandria, VA 22313-1450

Dear Commissioner:

It is respectfully requested that this application be accorded the benefits of Continued Examination in accordance with the provisions of 37 C.F.R. §1.114.

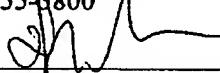
A submission meeting of requirements of 37 C.F.R. 1.111 accompanies this paper.

The required filing fee of \$790.00 for a large entity was charged to Deposit Account No. 50-3478 on July 31, 2007.

Please charge any additional fee or credit any overpayment to Deposit Account No. 50-3478.

Respectfully submitted,

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 Reg. No. 30,300

Dated: August 30, 2007

DPM/lsc/acp

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